

ABSTRACT OF DISCLOSURE

A liquid crystal display (LCD) panel including a liquid crystal panel, a first driving circuit, connected to the liquid crystal panel by a plurality of data lines, a second driving circuit, connected to the liquid crystal panel by a plurality of gate lines, an electrode pad unit applying a signal voltage aligning the liquid crystal filled in the liquid crystal panel, a first switching circuit, performing a switching operation to apply a part of the liquid crystal alignment signal voltage applied via the electrode pad unit to the liquid crystal panel via the data lines, a second switching circuit, performing a switching operation to apply a remaining part of the liquid crystal alignment signal voltage to the liquid crystal panel via the gate lines, and first and second buffer circuits, which prevent the liquid crystal alignment signal voltage from being applied backward to the first and second driving circuits.